

Bitsy Compact Flash Connector Board User's Manual

ADS document # 110111-80011



PRELIMINARY

Applied Data Systems

www.applieddata.net

9140 Guilford Road Columbia MD 21046 301-490-4007

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Revision History

ADS document # 110110-9001A.

REV	DESCRIPTION	DATE	BY
1	Initial release	06/21/01	HW

About the Cover Photo

The cover photo shows a Bitsy Connector board populated with optional Compact Flash.

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1 Introduction

1.1 Overview

The Bitsy Connector board is an optional unit intended to be used in conjunction with the Bitsy single board computer using the SA-1110 StrongARM RISC microprocessor. The Bitsy is designed to meet the needs of embedded and graphical systems developers.

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1.2 Organization of this Manual

The manual organizes information in three key sections:

Introduction	Provides an overview of the functionality and organization of the Bitsy Connector board.
Hardware Reference	Describes the configuration settings and connector pinouts for all systems of the Bitsy Connector board.
System Integration	Provides key information about power management, tips for system integration and electrical and mechanical interface specifications.

To locate the information you need, try the following:

- 1. Browse the *Table of Contents*. Section titles include connector designators and their function.
- 2. Follow cross-references between sections.
- 3. View and search this manual in PDF format

1.3 For Further Information...

ADS maintains a web site exclusively for its developers. The site includes downloads, troubleshooting guides, operating system updates and the "ADS Knowledge Base", a comprehensive document with dozens of questions answered about developing applications for ADS products. Instructions on how to access the site are shipped with every evaluation system.



2 Hardware Reference

This section gives an overview of the hardware features of the Bitsy Connector board. This overview includes a description of the switches, jumper settings, connector and connector pinouts.



2.1 Locating "Pin 1"

Many connectors and headers have a visible number on the board that indicates pin 1. If that pin is not clearly marked, look at the underside of the board. The square pad is pin 1.

2.2 Switches

2.2.1 SW1: Reset Switch

SW1 is the reset switch for the Bitsy. This switch issues a hardware reset to the SA-1110. Press this button to restart the Bitsy without cycling power. This operation will reset the real-time clock. Most operating systems clear the contents of DRAM upon hardware reset.

You can hold the Bitsy Connector Board in reset by pressing and holding this button.

2.3 Jumper Settings

Jumpers on the Bitsy Connector board select a variety of operational options. All use 2mm shorting blocks (shunts) to select settings. Make sure power is turned off to the Bitsy Connector board when changing the position of a shunt.

2.3.1 JP2 and JP3: COM3 signaling

Type: 2-post headers, 2mm

JP2 setting	JP3 setting	Connect
1-2	1-2	DCD DSR and DTR shorted together
1-2	None	DCD shorted to DTR
None	1-2	DSR shorted to DTR
None	None	No connection on DCD and DSR



2.3.2 JP4, JP5, JP6 and JP7 : COM2, IrDA connection

Type: 3-post headers, 2mm

There are three modes of operations for COM2 signals from the StrongARM CPU. They are IrDA (an IR transceiver is populated on the board), RS-232 or TTL level output serial port. For each one of thes operating modes, the following configuration has to be used:

JP4	JP5	JP6	JP7	COM2 configuration
1-2	1-2	2-3	2-3	COM2 at RS232 level
2-3	JP5.3 -	- JP6.2	2-3	COM2 at TTL level
None	None	1-2	1-2	IrDA active (J22 not used)
None	None	None	None	COM2 not used at all



2.3.3 J29: Chemistry and cells number selection

Type: 3-post header, 2mm

This connector board supports the charging capabilities of three different battery configurations. They can be selected through three jumper headers as shown in the table below:

J29	Function
1-2	Li_Ion 2 cells battery
2-3	NiCd/NiMH 5 cells battery
none	NiCd/NiMH 6 cells battery

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2.3.4 J30 and J32: Battery maximum voltage settings adjustment

2-post header, 2mm for J30 and J32 Type:

This connector board supports the charging capabilities of three different battery configurations. They can be selected through three jumper headers as shown in the table below:

J30	J32	Function
1-2	None	Maximum voltage set about 5% higher
none	1-2	Maximum voltage set about 5% lower
none	None	Maximum voltage set on standardl values
1-2	1-2	Not Allowed. It is possible to damage the system by using this configuration.



2.3.5 J37, J38: USB Connection

Type: 2-post headers, 2mm

It is possible, with this board, to use the Master USB wires and route them oto the slave connector. This was done to provide support for the Bitsy board Rev. A. The selection is made according the following table:

J37	J38	Connection
1-2	1-2	USB Slave uses same line of USB master
none	none	USB slave uses its own line



Connector Pinouts 2.4

The following tables describe connector pinouts and the type of connector. At least one pin of every connector is labeled on the Bitsy Connector board. Double-row headers on the board are all numbered as shown in the figure to the right.

2	4	6	8 7	
1	5	5	/	

For information about the location of the connectors on the Bitsy Connector board, refer to section Error! Reference source not found.. Error! Reference source not found.:

Legend: n/c Not connected

GND Bitsy Connector board ground plane

2.4.1 J2 Speaker Mono

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

A speaker may be connected across the speaker outputs (bridge-tied load). Minimum speaker impedance is eight ohms. Voltages on the speaker outputs are 0-3.3V. If you connect one side of the speaker to ground, you must use a blocking capacitor; in this case, output power will be reduced to one quarter and power consumption will be cut in half from that of the bridge-tied load. Consult the UCB1200 specifications for further details.

Pin	Signal name	Description
1	SPK -	Speaker connection (-)
2	SPK +	Speaker connection (+)

2.4.2 J4: Backup Battery Input

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

Pin	Signal name	Description
1	VBATNEG	
2	BATPOS	

2.4.3 J5: PS/2 Keyboard Input

Type: Mini DIN-6 socket housing, MDI-004-6PC

Recommended mating connector: PS/2 keyboard

Pin	Signal name	Description
1	TPSIG	PS/2 trackpad/keyboard data
2	N/C	not connected
3	GND	Ground
4	VCC	+ 5 Volts, fused at 350 mA
5	TPCLK	PS/2 trackpad/keyboard clock
6	N/C	not connected

2.4.4 J6: Speaker Left Stereo

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

A speaker may be connected across the speaker outputs (bridge-tied load). Minimum speaker impedance is eight ohms. Voltages on the speaker outputs are 0-3.3V. If you connect one side of the speaker to ground, you must use a blocking capacitor; in this case, output power will be reduced to one quarter and power consumption will be cut in half from that of the bridge-tied load. Consult the UCB1200 specifications for further details.

Pin	Signal name	Description
1	SPK1 -	Speaker connection (-)
2	SPK1 +	Speaker connection (+)

2.4.5 J7: Battery Power Input

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

This connector allows you to connect an external battery to back up power on the Bitsy. If no other power source is available, the system will draw its sleep mode current from this connection. There is a steady-state $1M\Omega$ load across these terminals. For reliable operation of the Bitsy, place a 0.100" shunt across the pins of this connector.

Pin	Signal name	Description
1	BATNEG	Bitsy ground
2	BATPOS	3 Volt battery, positive terminal

2.4.6 J11: USB Slave

Type:	Single	height	right	angle	USB	Туре	B
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Pin	Signal name	Description
1	VCC_OUT	
2	USB_UDC-	USP Slave
3	USB_UDC+	USB Stave
4	USB_GND	

2.4.7 J12: Backlight Inverter

Type: 7-pin header, 1.25mm, keyed, Molex #53261-0790

Recommended mating connector: Molex 51021-0700 or Quadrangle Products kit #RT51021-0700-18

This connector will supply power to a Xentek LS520 backlight inverter and compatible devices with a one-to-one cable. The onboard Bitsy Connector board PWM driver can electronically control the brightness of the inverter.

Pin	Signal name	Description
1	$\pm 12V$	0.12 walts from 15 4
2	$\pm 12V$	0-12 <i>voits from </i> 5 .4
3	GND	Ground
4	UND	Ground
		Open-collector output with 12V
5	BACKLIGHTON	pullup to control backlight (shorted
		to GND when "off")
	BACKLIGHTPWM	PWM brightness control (SA-1111)
6		(3.3V CMOS, 1.2k Ω series, 1 μ F
		filter)
7	GND	Ground

2.4.8 J13: LCD Panel (IDC 34)

Type: 34-pin IDC header, 2x17 0.100-inch spacing, shrouded, keyed

Recommended mating connector: IDC connector AMP #1-746288-8

This connector houses the signals to drive an 18-bit flat panel. Electrical specifications for panel signals are listed in section **Error! Reference source not found.**

Pin	Signal name	Description
1	PNL_VEE	VEE (contrast); see VR1, J19 and JP12
2	PNL_GND	Ground
3	PNL_PIXCLK	Pixel Clock
4	PNL_HSYNC	Horizontal Sync.
5	PNL_VSYNC	Vertical Sync.
6	PNL_GND	Ground
7	PNL_RED0	Red Bit 0 (same as RED5)
8	PNL_RED1	Red Bit 1
9	PNL_RED2	Red Bit 2
10	PNL_RED3	Red Bit 3
11	PNL_RED4	Red Bit 4
12	PNL_RED5	Red Bit 5
13	PNL_GND	Ground
14	PNL_GREEN0	Green Bit 0
15	PNL_GREEN1	Green Bit 1
16	PNL_GREEN2	Green Bit 2
17	PNL_GREEN3	Green Bit 3
18	PNL_GREEN4	Green Bit 4
19	PNL_GREEN5	Green Bit 5
20	PNL_GND	Ground
21	PNL_BLUE0	Blue Bit 0 (same as BLUE5)
22	PNL_BLUE1	Blue Bit 1
23	PNL_BLUE2	Blue Bit 2
24	PNL_BLUE3	Blue Bit 3
25	PNL_BLUE4	Blue Bit 4
26	PNL_BLUE5	Blue Bit 5
27	PNL_GND	Ground
28	PNL_LBIAS	Enable
29	DNI DWD	Vac(5V) or 2.2 V depending on ID1 position
30	PINL_PWK	vcc(5v) or 5.5 v, depending on JP1 position
31	PNL_RL	Horizontal Mode Select (set by JP2)
32	PNL_UD	Vertical Mode Select (set by JP3)
33	PNL_ENA	Panel enable signal (StrongARM GPIO24)
34	VCON	Adjustable voltage for passive panels; set with VR2 or fixed voltage divider

2.4.9 J14: Temperature Probe

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

Pin	Signal name	Description
1	SENSOR -	
2	SENSOR+	

2.4.10 J15: Touch Panel

Type: 4-pin header, 0.100-in spacing, friction lock, Molex 22-23-2041

Recommended mating connector: Molex 22-01-3047

The mapping shown of signal to edge of touch panel is an ADS convention. Interface cables are constructed to match this order. Some operating systems can tolerate swapping of positive (P) and negative (M) connections, and even swapping of X and Y pairs.

Pin	Signal name	Description
1	TSMX	Left edge of panel
2	TSPX	Right
3	TSPY	Bottom
4	ТРМҮ	Тор

2.4.11 J16: DC Power Input

Type: 3-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

Pin	Signal name	Description
1	+12V_IN	
2	DCIN_POS	
3	DCIN_NEG	

2.4.12 J17: +30V Power Input

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

Pin	Signal name	Description
1	Ground	
2	+30V	

2.4.13 J18: Serial Port 3

Type: 2x5 header, 0.100" IDC, keyed

Recommended mating connector: 2x5 IDC

Pin	Signal name	Description
1	<i>"DCD3"</i>	DCD from external device
2	"DSR3"	DSR from external device
3	RXD3	
4	RTS3	SA 1110 Savid Port 3 PS 232
5	TXD3	5A-1110 Senui 1 0h 5, K5-252
6	CTS3	
7	<i>"DTR3"</i>	DTR from external device
8	n/c	not connected
9	GNDFILT3	filtered connection to ground
10	n/c	not connected

Note: The SA-1110 does not have hardware handshaking on its serial lines. The Bitsy Connector board is wired to use SA-1110 GPIO lines to perform CTS/RTS handshaking.

2.4.14 J19: Internal Power Planes Connector

Type: Mini DIN-6 socket housing, MDI-004-6PC

Recommended mating connector: PS/2 keyboard

Pin	Signal name	Description
1	VCC	+5V
2	GROUND	GND
3	+3.3V	+3.3V Digital IO plane
4	+12V_IN	For Backlight inverter
5	+1.75V	For SA-1110 core logic
6	PWR_EN	POWERENABLE signal generated by the SA-1110

2.4.15 J20: Serial Port 1

Type: 2x5 header, 0.100" IDC, keyed

Recommended mating connector: 2x5 IDC

Pin	Signal name	Description
1	"DCD1"	DCD from external device
2	"DSR1"	DSR from external device
3	RXD1	
4	RTS1	SA 1110 Savid Port 1 PS 232
5	TXD1	5A-1110 Senut 1 0n 1, K5-252
6	CTS1	
7	<i>"DTR1"</i>	DTR from external device
8	RIB1	
9	GNDFILT1	filtered connection to ground
10	n/c	not connected

Note: The SA-1110 does not have hardware handshaking on its serial lines. The Bitsy Connector board is wired to use SA-1110 GPIO lines to perform CTS/RTS handshaking. J22: Serial Port 2

2.4.16 J22: Serial Port 2

Type: 2x5 header, 0.100" IDC, keyed

Recommended mating connector: 2x5 IDC

Pin	Signal name	Description
1	n/c	not connected
2	n/c	not connected
3	RXD2	
4	n/c	SA 1110 Sovial Port 2 TTI
5	TXD2	SA-1110 Senai 1 on 2, 11L
6	n/c	
7	n/c	not connected
8	n/c	not connected
9	GNDFILT2	filtered connection to ground
10	n/c	not connected

Note: The SA-1110 does not have hardware handshaking on its serial lines. The Bitsy Connector board is wired to use SA-1110 GPIO lines to perform CTS/RTS handshaking.

2.4.17 J28: Compact Flash

The 50-pin compact flash socket conforms to the compact flash standard for 3.3V and 5V Type II cards. It will supply up to 500 mA of 5V current. The socket is normally de-energized; the operating system is responsible for turning on the socket when a card is inserted and turning it off when the card is removed. The socket includes an integral card ejector.

2.4.18 J31: Spare UCB1200 and ADSmart IO™

Type: Mini DIN-40 socket housing,

Pin	Signal name	Description
1	UCB_IO0	General purpose IO available through the UCB1200
2	EXT_IRQ1	Available external Interrupt
3	UCB_IO1	General purpose IO available through the UCB1200
4	EXT_IRQ2	Available external Interrupt
5	UCB_IO2	General purpose IO available through the UCB1200
6	SMTIO0	General purpose IO available through the AVR
7	UCB_IO3	General purpose IO available through the UCB1200
8	SMTIO1	General purpose IO available through the AVR
9	UCB_IO4	General purpose IO available through the UCB1200
10	SMTIO2	General purpose IO available through the AVR
11	UCB_IO5	General purpose IO available through the UCB1200
12	SMTIO3	General purpose IO available through the AVR
13	UCB_IO6	General purpose IO available through the UCB1200
14	ANINO	Analog Input connected to the AVR
15	UCB_IO7	General purpose IO available through the UCB1200
16	ANIN1	Analog Input connected to the AVR
17	UCB_IO8	General purpose IO available through the UCB1200
18	ANIN2	Analog Input connected to the AVR
19	UCB_IO9	General purpose IO available through the UCB1200
20	ANIN3	Analog Input connected to the AVR
21	AGSTAT	AGC Status signal from UDA1341
22	VREF	Available VREF signal from UDA1341
23	CODEC_OFL	Overflow signal from the UDA1341
24	SHON_RS232	
25	AMP_SDWN	Shutdown signal fro the Audio codec UDA1341
26	SW2	Connection to Bitsy S1 switch 2
27	QMUTE	Quick Mute option for the UDA1341
28	SW3	Connection to Bitsy S1 switch 3
29	GPIO27_CLK	
30	ROW0	ROW0 kaypad signal
31	COLO	COL0 keypad signal
32	ROW1	ROW1 kaypad signal
33	COL1	COL1 keypad signal
34	ROW2	ROW2 kaypad signal
35	COL2	COL2 keypad signal
36	ROW3	ROW3 kaypad signal
37	COL3	COL3 keypad signal
38	ROW4	ROW4 kaypad signal
39	N/Cc	Not connected
40	GROUND	GND

2.4.19 J33: Microphone Mono

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

Input impedance of the microphone connection is 25 k Ω . Specifications for the UCB1200 call for 1k Ω or electret microphones. Microphones with other impedance may be used. Microphones connected should use a DC blocking capacitor between the microphone and MIC+ input.

Pin	Signal name	Description
1	MIC GND	Microphone (-)
2	MIC +	Microphone (+)

2.4.20 J34: Speaker Right Stereo

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

A speaker may be connected across the speaker outputs (bridge-tied load). Minimum speaker impedance is eight ohms. Voltages on the speaker outputs are 0-3.3V. If you connect one side of the speaker to ground, you must use a blocking capacitor; in this case, output power will be reduced to one quarter and power consumption will be cut in half from that of the bridge-tied load. Consult the UCB1200 specifications for further details.

Pin	Signal name	Description
1	SPK2 -	Speaker connection (-)
2	SPK2 +	Speaker connection (+)

2.4.21 J35: Microphone Stereo

Type: 3-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

Input impedance of the microphone connection is 25 k Ω . Specifications for the UCB1200 call for 1k Ω or electret microphones. Microphones with other impedance may be used. Microphones connected should use a DC blocking capacitor between the microphone and MIC+ input.

Pin	Signal name	Description
1	MIC 1_IN_LF	Left Microphone
2	GROUND	Ground
3	MIC2_IN_RT	Right Microphone

2.4.22 J36: Wake Up

Type: 2-pin header, 0.100-in spacing, friction lock, Molex 22-23-2021

Recommended mating connector: Molex 22-01-3027

	Pin	Signal name	Description
Γ	1	GROUND	Common GND
Γ	2	RQONOFF	Wake up interrupt signal to the SA-1110

2.4.23 J39: USB Master

Type: Single USB socket

Recommended mating connector: USB cable

Pin	Signal name	Description
1	VCC_OUT	
2	USB-	USP Master
3	USB+	USB master
4	USB_GND	

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3 Board Revision History

3.1.1 Identifying the board revision

The product revision number of the Bitsy Connector board is etched on the underside of the printed circuit board. That number is 170111-8000x, where "x" is the board revision.

3.1.2 Revision History

The following are the most significant changes that have occurred.

Rev. 1:

Initial release of the board

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